# INTERNATIONAL SEARCH REPORT

International Application No

PCT/GB 03/02822

**10/518556**

## A. CLASSIFICATION OF SUBJECT MATTER
IPC 7   G06F9/45

According to International Patent Classification (IPC) or to both national classification and IPC

## B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)
IPC 7   G06F

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal, INSPEC, IBM-TDB

## C. DOCUMENTS CONSIDERED TO BE RELEVANT

| Category° | Citation of document, with indication, where appropriate, of the relevant passages | Relevant to claim No. |
|---|---|---|
| X | JAN HOOGERBRUGGE: "Code Generation for Transport Triggered Architectures" 5 February 1996 (1996-02-05), TECHNISCHE UNIVERSITEIT DELFT , XP002303070 ISBN: 90-9009002-9 page 4 – page 5; figure 1.2 page 26 – page 28; figures 2.8,2.9 page 38 – page 40 page 46 – page 50; figure 3.3 | 1-27 |
| X | WO 01/59593 A (XILINX INC) 16 August 2001 (2001-08-16) abstract | 1,27 |
| X | US 5 507 030 A (SITES RICHARD L) 9 April 1996 (1996-04-09) abstract | 1,27 |
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[X] Further documents are listed in the continuation of box C.          [X] Patent family members are listed in annex.

° Special categories of cited documents :

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| Date of the actual completion of the international search | Date of mailing of the international search report |
|---|---|
| 28 October 2004 | 23/11/2004 |

| Name and mailing address of the ISA | Authorized officer |
|---|---|
| European Patent Office, P.B. 5818 Patentlaan 2 NL – 2280 HV Rijswijk Tel. (+31–70) 340–2040, Tx. 31 651 epo nl, Fax: (+31–70) 340–3016 | Härdeman, J |

| C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT | | |
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| A | CORPORAAL H: "Design of transport triggered architectures" VLSI, 1994. DESIGN AUTOMATION OF HIGH PERFORMANCE VLSI SYSTEMS. GLSV '94, PROCEEDINGS., FOURTH GREAT LAKES SYMPOSIUM ON NOTRE DAME, IN, USA 4-5 MARCH 1994, LOS ALAMITOS, CA, USA,IEEE COMPUT. SOC, 4 March 1994 (1994-03-04), pages 130-135, XP010098673 ISBN: 0-8186-5610-7 the whole document | 1-27 |
| A | CORPORAAL H: "TTAs: Missing the ILP complexity wall" JOURNAL OF SYSTEMS ARCHITECTURE, ELSEVIER SCIENCE PUBLISHERS BV., AMSTERDAM, NL, vol. 45, no. 12-13, June 1999 (1999-06), pages 949-973, XP004169209 ISSN: 1383-7621 the whole document | 1-27 |
| A | JANSSEN J ET AL: "PARTITIONED REGISTER FILE FOR TTAS" PROCEEDINGS OF THE 28TH. ANNUAL INTERNATIONAL SYMPOSIUM ON MICROARCHITECTURE. ANN ARBOR, NOV. 29 - DEC. 1, 1995, PROCEEDINGS OF THE ANNUAL INTERNATIONAL SYMPOSIUM ON MICROARCHITECTURE, LOS ALAMITOS, IEEE COMP. SOC. PRESS, US, vol. SYMP. 28, 29 November 1995 (1995-11-29), pages 303-312, XP000585374 ISBN: 0-8186-7349-4 the whole document | 1-27 |
| A | HOOGERBRUGGE J ET AL: "COMPARING SOFTWARE PIPELINING FOR AN OPERATION-TRIGGERED AND A TRANSPORT-TRIGGERED ARCHITECTURE" COMPILER CONSTRUCTION. INTERNATIONAL CONFERENCE, XX, XX, 5 October 1992 (1992-10-05), pages 219-228, XP000671931 the whole document | 1-27 |

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